

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

REDUCED DIELECTRIC CONSTANT SPACER MATERIALS
INTEGRATION FOR HIGH SPEED LOGIC GATES

Application Number :

Confirmation Number:

First Named Applicant: Michael Belyansky

Attorney Docket Number: FIS920040047US1

Art Unit:

Examiner:


Search string: (6528372 or 6342450 or 6316348 or 6365451 or 6194748 or 6297115 or 6383951
.pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
WLL	1	6528372	2003-03-04	Lukanc, et al.			
WLL	2	6342450	2002-01-29	Lattard			
WLL	3	6316348	2001-11-13	Fu et al.			
WLL	4	6365451	2002-04-02	Havemann			
WLL	5	6194748	2001-02-27	Yu			
WLL	6	6297115	2001-10-02	Yu			
WLL	7	6383951	2002-05-07	Li			

Signature

Examiner Name	Date
	6/7/05